Abstract

Method for controlling semiconductor chips and control apparatus

The invention relates to a method for operating semiconductor chips, particularly memory chips, which are arranged in groups on modules (M1-M4) which are connected to a common data bus (DQ),
where each semiconductor chip (IC1-IC36) on each module (M1-M4) is connected to at least one data line (DQ1-DQ72) in the data bus (DQ),
having the following method steps:
a) a group of semiconductor chips (IC1-IC36) is selected from semiconductor chips (IC1-IC36) arranged on the modules (M1-M4) on the basis of a prescribed selection criterion independently of module, the selected group of semiconductor chips (IC1-IC36) using the data lines (DQ1-DQ72) in the data bus (DQ) over the entire bus width;
b) the semiconductor chips (IC1-IC36) in the selected group are activated; and
c) data interchange is performed between the data lines (DQ1-DQ72) in the data bus (DQ) and the selected group of semiconductor chips (IC1-IC36).

Figure 3